Electronic Acknowledgement Receipt				
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International Application Number:				
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Title of Invention:	Circuit Arrangement and Method For Protecting an Integrated Semiconductor Circuit			
First Named Inventor/Applicant Name:	Bernd Deutschmann			
Customer Number:	26161			
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /₊zip	Pages (if appl.)
1 Information Disclosure Statement (IDS) Filed (SB/08)	026ids.pdf	310802	no	2	
		334a81edc9061ddd4b617f8290ad84a793e 5db50			

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2 Foreign Reference	Foreign Reference	JP526200.pdf	502238	no	19	
	31 320200.pd1	15cb4be43d903d1f2b36c3b90dc365e8aeb 5a842	110	19		
Warnings:				-		
Information:						
3 Foreign Reference	KR8809.pdf	273876	no	6		
	integer, pui	bb382bcccc8965d02ca234f8634aed7eb9c 3767f				
Warnings:						
Information:						
4 NPL Documents	examreport.pdf	507884	no	13		
	Cxumicport.pu	6b11028f9bcc15505860631894dc51b9b1f ebdc0	110			
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National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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